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APPLICATION NO.	FILING DATE		FIRST NAMED INVENTOR		ATTORNEY DOCKET NO.	CONFIRMATION NO.			
10/815,574	04/01/2004		Sang-bo Lee		5649-1255	9388			
TITLE OF INVENTION:	MULTI-STAGE OUTPUT	MULTIPLEXIN	G CIRCUITS AN	METHODS FOR D	OUBLE DATA RATE SY	NCHRONOUS MEMO			
DEVICES						Trointorto op 1/22/10			
APPLN. TYPE	SMALL ENTITY	ISSUE F	EE I	UBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE			
nonprovisional	NO	\$1400)	\$300	\$1700	06/01/2005			
EXAMINER		ART UN	TIT C	LASS-SUBCLASS	1				
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Typed or printed name _	Rohan G. Sabapa	thypillai		Registration	No. 51,074				
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